IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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P. 12

Applicant:

Chambers

Art Unit:

2829 Sarkar, A.

APR 2 0 2005

Serial No.:

10/649,425

Examiner: Docket No.: TI-35714

Filing Date: 08/27/2003

Conf. No.:

5169

Customer No.: 23494 Title: STRUCTURE AND

METHOD

Jacqueline J Garner

TO FABRICATE SELF-ALIGNED

TRANSISTORS WITH DUAL WORK FUNCTION METAL GATE ELECTRODES

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by e to the U.S. Patent and Trademark Office at 703-872-9306 on

J. Garner, Reg. No. 36,144

April 20, 2005 Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

☐ One Month (\$120)

☐ Two Months (\$450)

☐ Three Months (\$1,020)

☐ Four Months (\$1,590)

Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668.

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348

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Respectfully Cubmitted,

Jacqueline J. Gamer Reg. No. 36,144

PAGE 12/13 * RCVD AT 4/20/2095 11:35:20 AM [Eastern Daylight Time] * SVR:USPTO-EPXRF-1/1 * DMS:8729306 * CSID:972 278 9775 * DURATION (mm-4s):03-52